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	下での氏名の発明者として、私は以下の通り宣言します。	As a below namd inventor, i hereby declar 'hat:
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	(下記の名称が複数の場合)信じています。	for which a patent is sought on the invention entitled SIGNAL PROCESSING CIRCUIT INIFERATING FULSE WIDTHS OF AN INPUT FULSE
_		SIGNAL ACCORDING TO POLARITIES
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Prior Foreign Application(s)

外国での先行出願Patent Applications No. 2001-044223 Japan

(Number)	(Country)
(番号)	(闰名)
(Number)	(Country)
(番片)	(闰名)
No.2001-333102	Japan
No.2001-044223	Japan

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Priority Not Claimed 優先様上導か1

20/February/2001	優先権主張なし
(Day/Month/Year Filed) (出版年月日) 30/October/2001	- 0
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委任状: 私は下記の発明者として、本出額に関する一切の 子続きを米特許商標局に対して遂行する弁理士主たは代理人 として、下記の者を指名いたします。(弁護士、または代理 人の氏名及び登録番号を明記のこと)

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith (list name and registration number)
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212-278-1000

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(Supply similar information and signature for third and subsequent joint inventors.)